# RECOMMENDED BOOKS FOR M.TECH JNTUH

## EMBEDDED SYSTEMS

### Hardware/Software Co-Design (I Year II Semester)

**Hardware/Software Co-Design: Principles and Practice**  
Jørgen Staunstrup and Wayne Wolf  
Rpt. 2009 395 pp 978-81-8128-684-0 BSPSPR *Rs. 825.00

### Digital Signal Processors and Architecture (I Year II Semester)

**Digital Signal Processors and Architecture**  
Phil Lapsley et al.  
2010 210 pp 978-81-265-2354-2 BSPJ/W *Rs. 575.00

### CPLD and FPGA Architectures and Applications (I Year II Semester)

**Field-Programmable Gate Array Technology**  
Stephen M. Trimberger  
Contents: 1. Introduction 2. SRAM Programmable FPGAs 3. Antifused Programmed FPGAs 4. Erasable Programmable Logic Devices  
Rpt. 2007 258 pp 978-81-8128-603-1 BSPSPR *Rs. 750.00

## Other Relevant Books

### Embedded Systems Building Blocks, 2nd Ed.

**Embedded Systems Building Blocks, 2nd Ed.**  
Jean J. Labrosse  
Rpt. 2011 611 pp 978-93-80501-89-5 BSPELS Rs. 950.00

### Designing Embedded Processors: A Low Power Perspective

**Designing Embedded Processors: A Low Power Perspective**  
Henkel, Jörg; Parameswaran  
Rpt. 2011 550 pp 978-81-84898-47-7 BSPSPR Rs. 650.00

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